

**In the Specification**

At page 1, the paragraph inserted before the Technical Field by the amendment dated August 14, 2001, has been amended as shown below:

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**CROSS REFERENCE TO RELATED APPLICATION**

Bl This patent application is a Continuation Application of U.S. Patent Application Serial No. 08/842,230, filed on April 22, 1997, now U.S. Patent No. 6,297,129, entitled "Memory Integrated Circuitry" and naming Luan Tran and Alan R. Reinberg as inventors, the disclosure of which is hereby incorporated herein by reference.

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